

# High Performance Modelling and Simulation for Big Data Application

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UNIVERSITEIT VAN AMSTERDAM

ISC High Performance  
Workshop on HPC I/O in the Data Center  
(HPC-IODC 2017)  
Frankfurt (Main), Germany  
June 22, 2017

# Disclaimer

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**This is NOT a research talk.**

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## Disclaimer 2:

**This is NOT primarily about HPC I/O.**

## High Performance Modelling and Simulation for Big Data Application

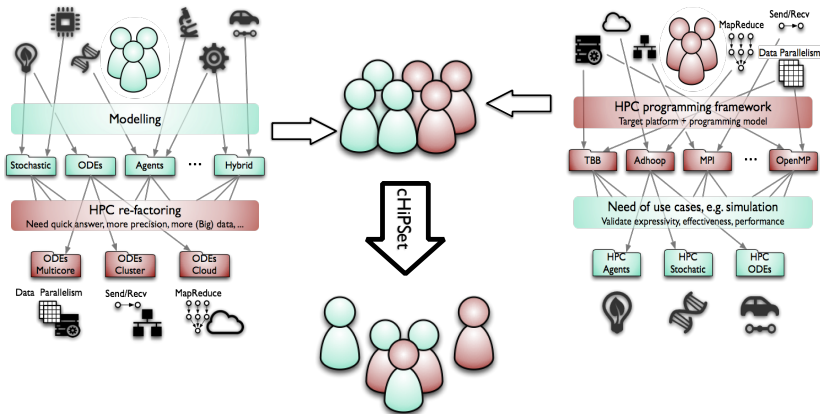


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### ICT COST Action:

- ▶ European funding instrument
- ▶ *Collaboration in Science and Technology*
- ▶ Recently integrated into Horizon 2020 framework

# What we aim for

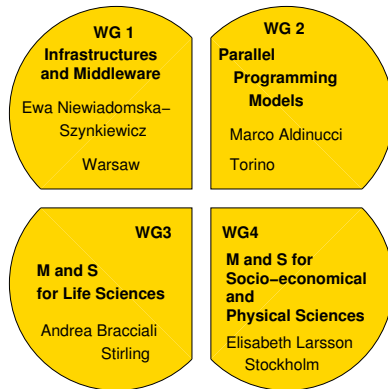


## Bringing together communities

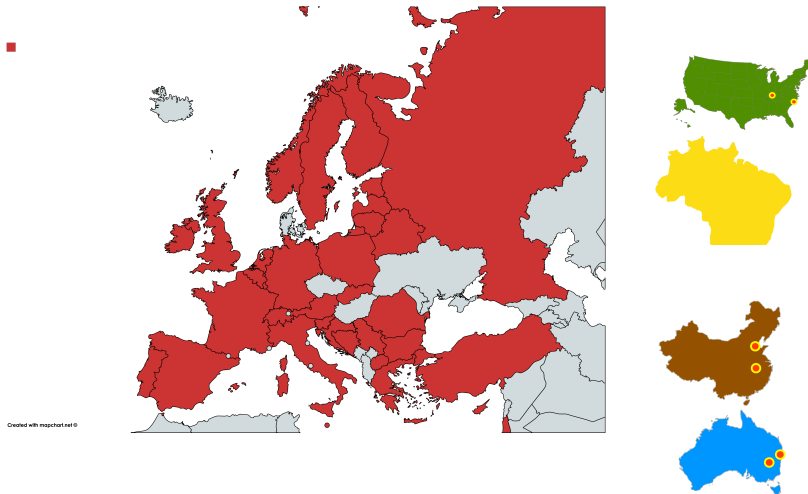
## Chairs:

- ▶ Joanna Kolodziej, Cracow, Action Chair
- ▶ Horacio González-Vélez, Dublin, Action Vice Chair
- ▶ Clemens Grelck, Amsterdam, Scientific Coordinator

## Work groups:



# Where we are



- ▶ 34 COST countries
- ▶ 4 associated countries

# Who we are altogether



- ▶ 100+ researchers, domain experts, practitioners, ...
- ▶ from academia and industry



## Bi-annual Action Meetings:

- ▶ Face-to-face discussion
- ▶ Application area groups

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## Countless virtual meetings:

- ▶ Systematic literature reviews
- ▶ Joint publications
- ▶ Joint project proposals
- ▶ ...

## Annual training school:

- ▶ Experienced researchers share their views
- ▶ with PhD students and early career investigators
- ▶ on a specific topic:
  - ▶ 2016: New Trends in Modelling and Simulation in HPC Systems, Bucharest, Romania
  - ▶ 2017: Big Data Processing and Analytics in the Internet of Everything Era, Novi Sad, Serbia

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## Short term scientific missions:

- ▶ Researcher mobility between Action partners
- ▶ One to several weeks exchange
- ▶ Priority for PhD students and early career investigators

**Cool Stuff !!**

**Can I still join ??**

## Cool Stuff !!

### Can I still join ??

- ▶ Countries **NO**
- ▶ People **YES**

See **[www.chipset-cost.eu](http://www.chipset-cost.eu)** for details and procedure.



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